

FORM PTO-1449 <i>RE JCA</i> OCT 06 2003 U.S. PATENT AND TRADEMARK OFFICE		Atty. Docket No.: P440.12-0006	Application No.: 10/616,801
LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT		First Named Inventor: Kurt Kimber et al.	
		Filing Date: July 10, 2003	Group Art: <u>2936</u>

U.S. PATENT DOCUMENTS

Examiner Initials		Document No.	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Documents
<i>RHJ</i>	AA	6,429,489	08-06-2002	Botula et al. <u>257/355</u>
<i>RHJ</i>	AB	6,549,061	04-15-2003	Voldman et al. <u>327/483</u>
	AC			
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	AI			
	AJ			
	AK			

FOREIGN PATENT DOCUMENTS

		Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Documents	Translation Yes No
	AL				
	AM				
	AN				

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

<i>RHJ</i>	AO	"An On-Chip ESD Protection Circuit with Low Trigger Voltage in BiCMOS Technology", Albert Z. H. Wang and Chen-Hui Tsay, <i>IEEE Journal of Solid-State Circuits</i> , Vol. 36, No. 1, January 2001.
	AP	
	AQ	

EXAMINER:

Ronald M. Levy

DATE CONSIDERED:

9/28/2004

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.